ABSTRACT OF THE DISCLOSURE

An input circuit for preventing the application of a voltage exceeding a transistor withstand voltage when the input circuit is switched to a standby state. The input circuit includes a first differential amplification circuit powered by a first power supply to amplify a first input signal and generate a second input signal. A level shift circuit is powered by the first power supply to generate a shifted input signal from the second input signal. A second differential amplification circuit is powered by a second power supply to amplify the shifted input signal and generate an amplified signal. A current control circuit selectively switches the input circuit between activated and standby states. A first circuit charges or discharges the level shift circuit so that voltage of the shifted input signal is less than or equal to voltage of the second power supply when switched to the standby state.